

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
1	US 20030035492 A1	20030220	18	Symbol constellations having second-order statistics with cyclostationary phase	375/295	375/229; 375/296	Murphy, Charles Douglas
2	US 20010016003 A1	20010823	7	Error control apparatus and method for channel equalizer	375/232	375/350	Kim, Gang-Ho
3	US 6803829 B2	20041012	149	Integrated VCO having an improved tuning range over process and temperature variations	331/34	257/E27.046; 331/117 FE; 331/175; 331/186; 331/66	Duncan; Ralph et al.
4	US 6759904 B2	20040706	122	Large gain range, high linearity, low noise MOS VGA	330/254	257/E27.046; 327/359	Behzad; Arya R.
5	US 6525609 B1	20030225	123	Large gain range, high linearity, low noise MOS VGA	330/254	257/E27.046; 327/359	Behzad; Arya R.
6	US 6496229 B1	20021217	24	TV receiver using read-only memory shared during VSB and QAM reception for synchrodyning I-F signal to baseband	348/725	348/427.1; 348/433.1; 348/726; 348/731	Limberg; Allen LeRoy
7	US 6463295 B1	20021008	37	Power control with signal quality estimation for smart antenna communication systems	455/522	455/562.1; 455/69	Yun; Louis C.
8	US 6445039 B1	20020903	100	System and method for ESD Protection	257/355	257/356; 257/357; 257/360; 257/E27.046	Woo; Agnes N. et al.

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9	US 6426972 B1	20020730	26	Reduced complexity equalizer for multi mode signaling	375/229	375/233	Endres; Thomas J. et al.
10	US 6426680 B1	20020730	152	System and method for narrow band PLL tuning	331/34	257/E27 .046; 331/117 FE; 331/117 R; 331/16; 331/17; 331/179; 331/36C	Duncan; Ralph et al.
11	US 6421378 B1	20020716	23	Signal waveform equalizer apparatus	375/229	375/344	Fukuoka; Toshihiko et al.
12	US 6320904 B1	20011120	8	Clustering blind convergence process in an adaptive decision feedback equalizer	375/233	375/261	Velez; Edgar et al.
13	US 6188722 B1	20010213	11	Sequential blind convergence process in an adaptive decision feedback equalizer	375/233	333/28R	Velez; Edgar et al.
14	US 6005640 A	19991221	18	Multiple modulation format television signal receiver system	348/726	348/555; 348/558; 348/614; 348/720; 375/235; 375/240. 01; 375/348	Strolle; Christopher H. et al.

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15	US 5841816 A	19981124	18	Diversity Pi/4-DQPSK demodulation	375/331	375/332; 375/347; 375/366; 455/134; 455/137; 455/277. 2	Dent; Paul W. et al.
16	US 5825832 A	19981020	14	Method and device for the reception of signals affected by inter-symbol interface	375/341	714/795	Benedetto; Valter
17	US 5799037 A	19980825	18	Receiver capable of demodulating multiple digital modulation formats	375/233	375/326	Strolle; Christopher H. et al.
18	US 5774450 A	19980630	26	Method of transmitting orthogonal frequency division multiplexing signal and receiver thereof	370/206	370/208; 370/210; 370/515; 375/368	Harada; Yasuo et al.
19	US 5537439 A	19960716	10	Decision directed algorithm control method	375/232	375/229; 708/323	Choi; Yang-seok
20	US 5056117 A	19911008	14	Decision feedback equalization with trellis coding	375/234	2/917; 375/341; 375/349; 714/795	Gitlin; Richard D. et al.
21	US 4847797 A	19890711	13	Adaptive blind equalization method and device	708/3	333/18; 375/235; 708/323	Picchi; Giorgio et al.

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22	US 4646173 A	19870224	55	Converting and decoding receiver for digital data recorded in analog form on magnetic tape	360/51	360/32; 375/350; 375/371	Kammeyer; Karl-Dirk et al.
23	US 4344177 A	19820810	8	Equalizer comprised of equalizer sections which include internal accumulation circuits	375/236	708/323	Kustka; George J.
24	US 4237554 A	19801202	12	Coefficient tap leakage for fractionally-spaced equalizers	375/234	333/18; 708/323	Gitlin; Richard D. et al.
25	US 4097807 A	19780627	16	Automatic equalizing method and system	375/232	375/270; 375/344	Fujimura; Noriaki
26	US 4089061 A	19780509	16	Method and apparatus for determining the initial values of the coefficients of a complex transversal equalizer	708/305	333/18; 375/231; 375/261; 375/270	Milewski; Andrzej T.
27	US 4004226 A	19770118	11	QAM receiver having automatic adaptive equalizer	375/231	333/18; 375/261	Qureshi; Shahid U. H. et al.
28	US 20010016003 A	20010823		Combine G-pseudo channel equalizer for controlling error in digital transmission system, adds absolute value of real and imaginary portion of error calculated based on corrected signal to obtain absolute value of error			KIM, G H et al.